

FORM PTO-1449  
(REV. 7-80)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.  
**TI-31026**SERIAL NO.  
**TBD****LIST OF DOCUMENTS CITED BY APPLICANT**  
(Use several sheets if necessary)

APPLICANT

**BRODSKY, ET AL.**FILING DATE  
**TBD**GROUP  
**TBD**Jc813 U.S. PTO  
09/668999

09/25/00

## U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPROPRIATE)
DF	AA	5,440,162	8/8/95	Worley et al	257	355	
DF	AB	5,706,156	1/6/98	Narita	361	56	
DF	AC	5,729,419	3/17/98	Lien	361	111	
DF	AD	5,969,929	10/19/99	Kleveland et al	361	111	
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	AL							
	AM							
	AN							

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	Maloney, Intel Corporation "Designing MOS Inputs and Outputs to Avoid Oxide Failure in The Charged Device Model", 1988 EOS/ESD Symposium Proceedings
	AP	
	AQ	

EXAMINER

DATE CONSIDERED

10-15-01

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.